

<b>Form PTO-1449</b> (modified) List of Patents and Publications For Applicant's Information Disclosure Statement (Use several sheets if necessary)			ATTY. DKT. NO. 5580-01302  APPLICANT: James Y. Cho, et al.  FILING DATE: Herewith		SERIAL NO. <u>Unknown</u> 12/026,790 GROUP: <u>Unknown</u> 2187		
<b>U.S. PATENT DOCUMENTS</b>							
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
HP		5,619,471	4/97	Ann B. Nunziata	—	—	—
		5,241,665	8/93	James R. MacDonald	—	—	—
		5,278,967	1/94	Brian W. Curran	—	—	—
		5,307,320	4/94	Farrer, et al.	—	—	—
		5,329,629	7/94	Horst, et al.	—	—	—
		5,850,632	12/98	Iain Robertston	—	—	—
		5,895,481	4/99	Kok-Kean Yap	—	—	—
		6,032,214	2/00	Farmwald, et al.	—	—	—
		6,041,393	3/00	Wei Hsu	—	—	—
		6,049,855	4/00	Joseph Jeddeloh	—	—	—
		6,052,134	4/00	Joseph E. Foster	—	—	—
		6,154,821	11/00	Barth, et al.	—	—	—
		6,154,825	11/00	Murdoch, et al.	—	—	—
		6,505,269	1/03	Kenneth H. Potter	—	—	—
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO
<b>OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
		Halfhill, "SiByte Reveals 64-bit Core for NPUs," Microprocessor Report, June 2000, pp. 45-48.					
HP		<u>Digital Semiconductor 21172, Core Logic Chipset, Technical Reference Manual</u> , © Digital Equipment Corporation, April 1996, Ch. 3 pp. 17-27; Ch. 4 pp. 49-61.					
		82430 PCIsset Cache/Memory Subsystem, © 1993 Intel Corporation, pp. 53-168.					
		82420 PCIsset Cache/Memory Subsystem, © 1993 Intel Corporation, pp. 23-149.					
		U.S. Publication No. 2001/0005876, published June 2001, Srinivasan, et al.					

EXAMINER:

*Hugh D. Nguyen*

DATE CONSIDERED:

9/1/04

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Information Disclosure Statement--PTO 1449 (modified)

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Substitute for form 1449/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

**Complete if Known**

Application Number	10/626,790
Filing Date	07/24/2003
First Named Inventor	James Y. Cho
Art Unit	2186
Examiner Name	none assigned
Attorney Docket Number	BP1746CON

Sheet	1	of	1
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## U. S. PATENT DOCUMENTS

[illegible]

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No.	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	7
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				
HJ		GB 2 256 293 A	12-02-1992	Research Machines	—	
HJ		EP Search Report for	—	—	—	
HJ		EP app. 01308014.8	03-29-2004	Broadcom Corp.	—	

Examiner Signature	<i>Hiep T. Nguyen</i>	Date Considered	9/7/04
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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